# High Efficiency 2.5A Synchronous Boost Convertor

General Description

The LP6252 is a Synchronous current mode boost DC-DC converter. Its PWM circuitry with built-in 2.5A Current power MOSFET makes this converter highly power efficiently. Selectable high switching frequency allows faster loop response and easy filtering with a low noise output. The non-inverting input its error amplifier is connected to an internal 800mV precision reference voltage.. Current mode control and external compensation network make it easy and flexible to stabilize the system.
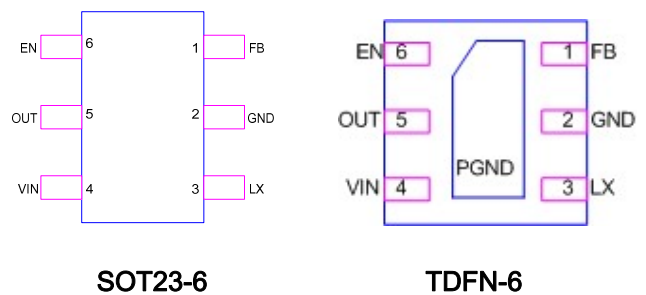
Ordering Information

LP6252 ☐ ☐ ☐

- F: Pb-Free
- Package Type
  - B6: SOT23-6
  - QV: TDFN-6
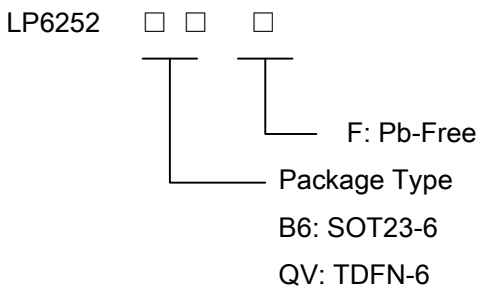
Applications

- ✧ Battery products
- ✧ Host OTG
- ✧ Audio power

Features

- ◆ Up to 95% efficiency
- ◆ Output to Input Disconnect at Shutdown Mode
- ◆ Shut-down current:<1uA
- ◆ Output voltage Up to 5.5V
- ◆ Over Output Voltage Protection
- ◆ Over Current Protection
- ◆ Over Temperature Protection
- ◆ Output voltage Up to 5.8V
- ◆ Internal Compensation of Soft-start
- ◆ 600KHz fixed frequency switching
- ◆ High switch on current:2.5A
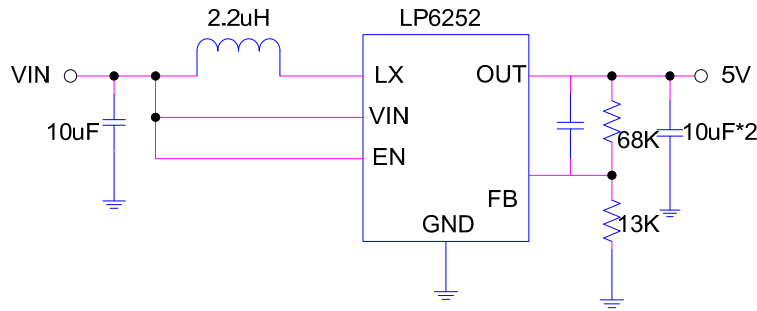- ◆ Available in SOT23-6 and TDFN-6 Package

Pin Configurations

SOT23-6   TDFN-6

Marking Information

Device	Marking	Package	Shipping
LP6252B6F	LPS 5UYWX	SOT23-6	3K/REEL
LP6252QVF	LPS LP6252 YWX	TDFN-6	

Typical Application Circuit

Functional Pin Description

PIN	PIN Name	Description
1	FB	Regulation Feedback Input. Connect to an external resistive voltage divider from the output to FB to set the output voltage. The pin is 0.8V.
2	GND	Ground Pin.
3	LX	Pin for switching.
4	VIN	Battery supply input pin.
5	OUT	Voltage output Pin.
6	EN	Open-drain Low Battery Detector output pin.

Function Block Diagram
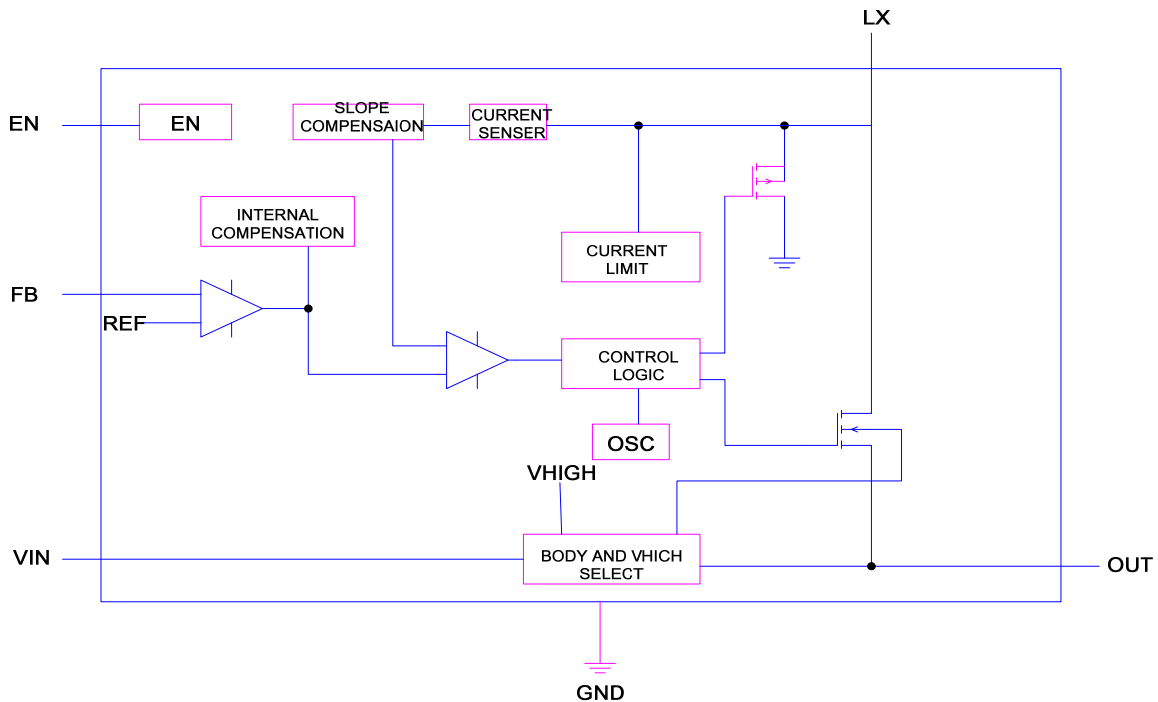
Absolute Maximum Ratings

Supply Input Voltage---6V

Power Dissipation, PD @ TA = 25°C

Package Thermal Resistance

SOT23-6, θJA ---110°C/W

TDFN-6, Θja--48°C/W

Lead Temperature (Soldering, 10 sec.) --260°C

Recommended Operating Conditions

Supply Input Voltage--2.2V to 6V

EN Input Voltage ---0V to 5.5V

Operation Junction Temperature Range -- --- −40°C to 125°C

Operation Ambient Temperature Range--- −40°C to 85°C

Electrical Characteristics

(Vin=3.3V,Vout=5V,Cin=10uF,Cout=22uF,L1=4.7uH,R1=52.5K,R2=10K)

| Parameter	Conditions	LP6252			Units
		Min	Typ	Max	
Supply Voltage		2.2		5.5	V
Output Voltage Range		2.5		5.5	V
UVLO		1.85			V
Over Output Voltage Protection			5.8		V
Supply Current(Shutdown)	VEN=VOUT=0V,VSW=5V		0.05	1	uA
Supply Current	VFB=0.9V		0.19		mA
Feedback Voltage		784	800	816	mV
Feedback Input Current	VFB=0.8V		50		nA
Switching Frequency			600		KHz
Maximum Duty Cycle		80	90	95	%
EN Input Low Voltage				0.4	V
EN Input High Voltage		1.4			V
Low-side Current Limit			2.5		A
High-side On Resistance	Vout=3.3V		215		$m\Omega$
Low-side On Resistance			145		$m\Omega$

Typical Operating Characteristics
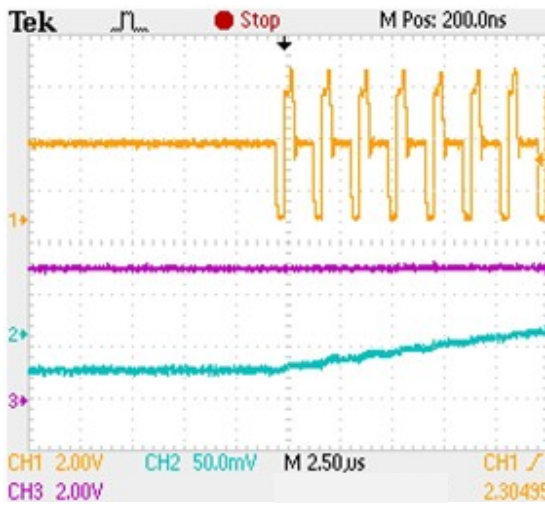
Vout=5V   Vin=3V   Iout = 0 m A
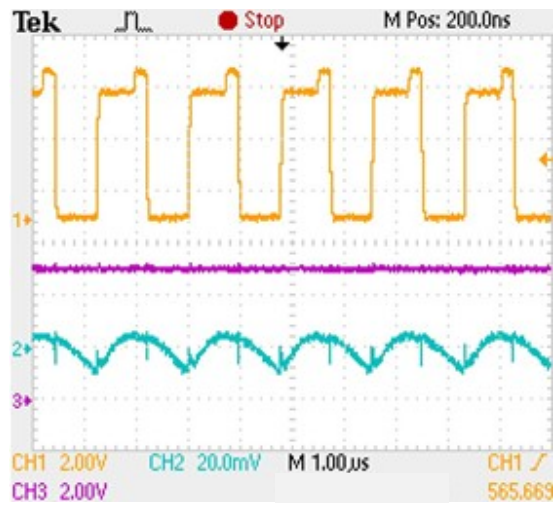
Vout=5V   Vin=3V   Iout = 5 0 mA
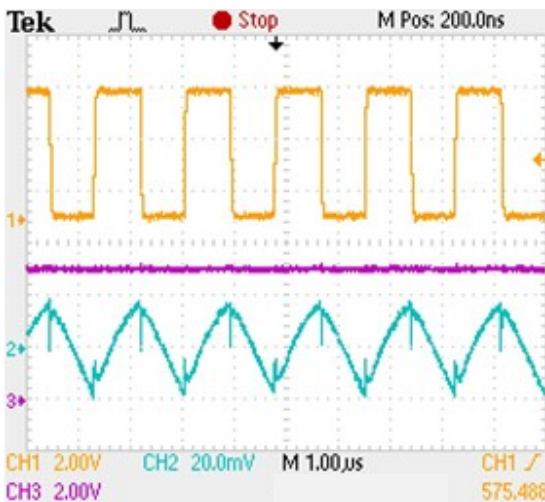
Vout=5V   Vin=3V   Iout = 500 m A

Vout=5V   Vin=3V   Iout = 1 A

Efficiency VS output current
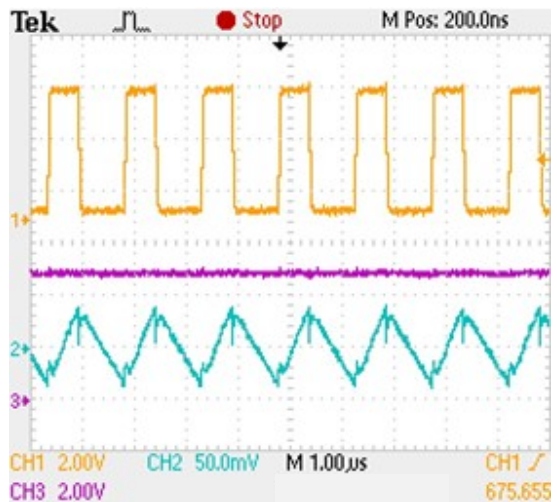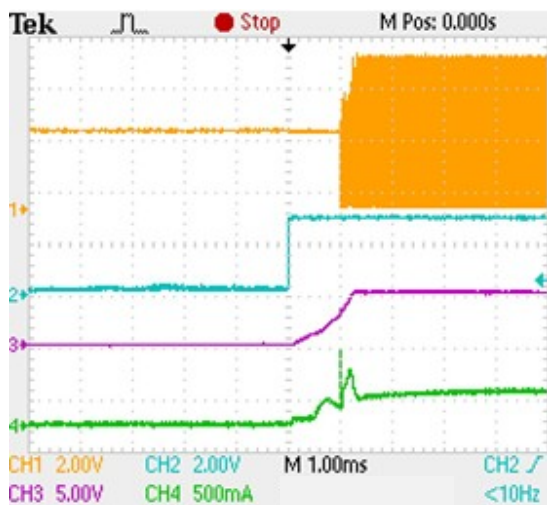
EN VS Vout
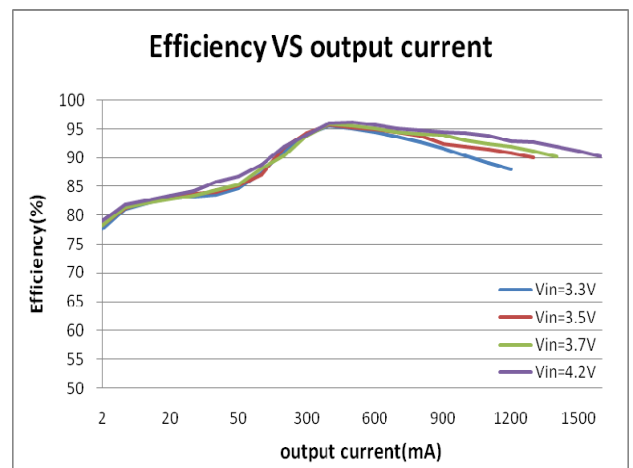
Operation Information

The LP6252 uses a synchronous 600KHz fixed frequency, current-mode regulation architecture to regulate the output voltage. The LP6252 measures the output voltage through an external resistive voltage divider and compares that to the internal 0.8V reference to generate the error voltage to the inductor current to regulate the output voltage. the use of current-mode regulation improves transient response and control loop stability.

When the LP6252 is disable (EN=Low),both power switches are off. There is no current path from SW to OUT. Therefore, the output voltage discharges to ground. When the LP6252 is enabled(EN=High),a limited start-current charges the output voltage rising to SW, then the part operates in force PWM mode for regulating the output voltage to the target value. At the beginning of each cycle, the N-channel MOSFET switch is turned on, forcing the inductor current to rise, The current at the source of the switch is internally measured and converted to a voltage by the current sense amplifier. That voltage is compared to the error voltage. When the inductor current rises sufficiently, the PWM comparator turns off the switch, forcing the inductor current to the output capacitor through the internal P-Channel MOSFET rectifier, which forces the inductor current to decrease. The peak inductor current is controlled by the error voltage. Thus the output voltage controls the inductor current to satisfy the lode.

Setting the Output Voltage

Set the output voltage by selecting the resistive voltage divider ratio. The voltage divider drops the output voltage to the 0.8V feedback voltage. Use a 100K resistor for R2 of the voltage divider. Determine the high-side resistor R1 by the equation:

$$Vout=(R1/R2+1) \times VFB$$

Pre-Boost Current and Short Circuit Protect

Initially output voltage is lower than battery voltage, and the LP6252 enters pre-boost phase. During pre-boost phase, the internal NMOSFET/PMOSFET is turned off/on and a constant current is provided from battery to output until the output voltage close to the battery voltage. The constant current is limited by internal controller. If the output short to ground, the LP6252 also limits the output current to avoid damage condition

Inductor Selection

For a better efficiency in high switching frequency converter, the inductor selection has to use a proper core material such as ferrite core to reduce the core loss and choose low ESR wire to reduce copper loss. The most important point is to prevent the core saturated when handling the maximum peak current. Using a shielded inductor can minimize radiated noise in sensitive applications. The maximum peak inductor current is the maximum input current plus the half of inductor ripple current. The calculated peak current has to be smaller than the current limitation in the electrical characteristics. A typical setting of the inductor ripple current is 20% to 40% of the maximum input current. If the selection is 40%, the maximum peak inductor current is

$$I_{PEAK} = I_{IN(MAX)} + \frac{1}{2}I_{RIPPLE} = 1.2 \times I_{IN(MAX)}$$

$$= 1.2 \times \left[\frac{I_{OUT(MAX)} \times V_{OUT}}{\eta \times V_{IN(MIN)}} \right]$$

The minimum inductance value is derived from the following equation :

$$L = \frac{\eta \times V_{IN(MIN)}^2 \times \left[V_{OUT} - V_{IN(MIN)} \right]}{0.4 \times I_{OUT(MAX)} \times V_{OUT}^2 \times f_{OSC}}$$

Depending on the application, the recommended inductor value is between 2.2µH to 4.7µH.

Input Capacitor Selection

For better input bypassing, low-ESR ceramic capacitors are recommended for performance. A 10µF input capacitor is sufficient for most applications. For a lower output power requirement application, this value can be decreased.

Output Capacitor Selection

For lower output voltage ripple, low-ESR ceramic capacitors are recommended. The tantalum capacitors can be used as well, but the ESR is bigger than ceramic capacitor. The output voltage ripple consists of two components: one is the pulsating output ripple current flows through the ESR, and the other is the capacitive ripple caused by charging and discharging.

$$V_{RIPPLE} = V_{RIPPLE\_ESR} + V_{RIPPLE\_C}$$

$$\cong I_{PEAK} \times R_{ESR} + \frac{I_{PEAK}}{C_{OUT}} \left(\frac{V_{OUT} - V_{IN}}{V_{OUT} \times f_{OSC}} \right)$$

Layout Guide

For high frequency switching power supplies, the PCB layout is important step in system application design. In order to let IC achieve good regulation, high efficiency and stability, it is strongly recommended the power components should be placed as close as possible. The set races should be wide and short. The feedback pin and then works of feedback and compensation should keep away from the power loops, and be shielded with a ground trace or plane to prevent noise coupling.
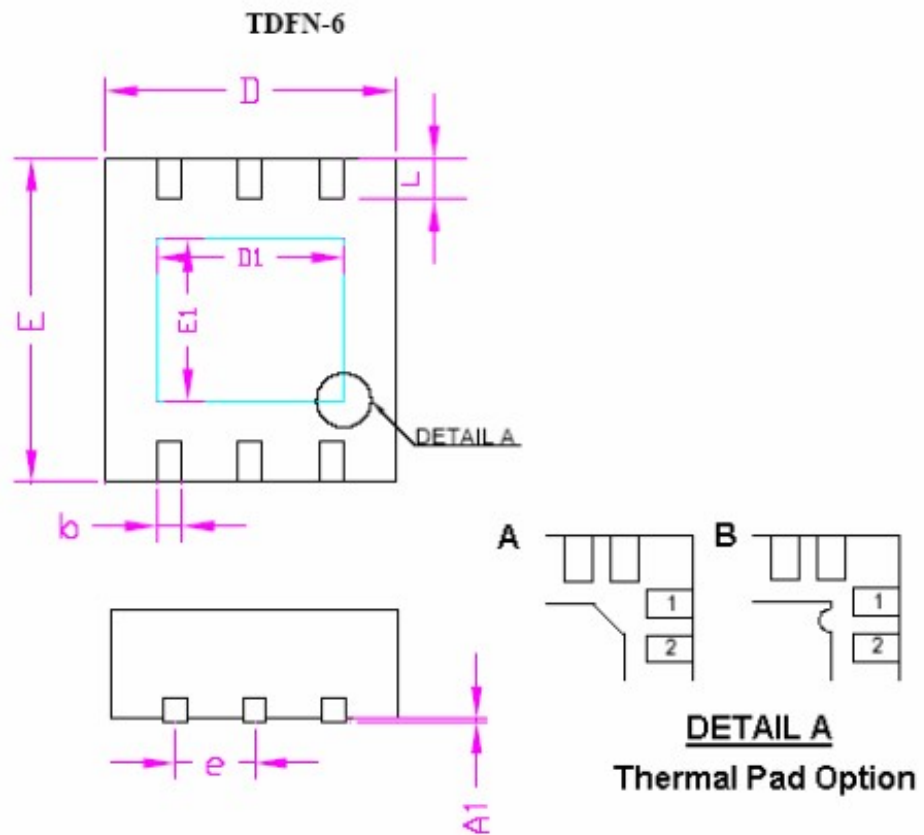
Packaging Information
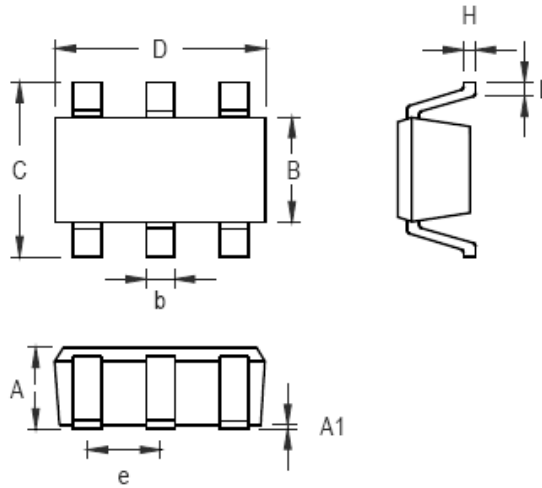
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.889	1.295	0.031	0.051
A1	0.000	0.152	0.000	0.006
B	1.397	1.803	0.055	0.071
b	0.250	0.560	0.010	0.022
C	2.591	2.997	0.102	0.118
D	2.692	3.099	0.106	0.122
e	0.838	1.041	0.033	0.041
H	0.080	0.254	0.003	0.010
L	0.300	0.610	0.012	0.024

SOT-23-6 Surface Mount Package

TDFN-6

DETAIL A
Thermal Pad Option

SYMBOLS	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
b	0.20	0.40	0.008	0.016
D	1.90	2.10	0.075	0.083
D1	1.40		0.055	
E	1.90	2.10	0.075	0.083
E1	0.80		0.031	
e	0.65		0.026	
L	0.25	0.45	0.010	0.018